

Notice of References Cited

Application/Control No.

10/687,958

Applicant(s)/Patent Under

Reexamination
BURKE ET AL.

Examiner

ALEX NOGUEROLA

Art Unit

1753

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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FOREIGN PATENT DOCUMENTS

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	Q					
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	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	pages 320-322 of Microsystem Design by Stephen Senturia, Kluwer Academic Publishers (2002) ("Microsystem Design").
	V	Bedell ("Admittance and Impedance Loci," Proc. Phys. Soc. London 14 327-336) 1895
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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Alex Noguera
6/22/07